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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/308,032	08/13/1999	BART DIERICKX	FILLF6.001AUS	5620

7590                  04/20/2005

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EXAMINER

GENCO, BRIAN C

ART UNIT

PAPER NUMBER

2615

DATE MAILED: 04/20/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

WATK

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	09/308,032	DIERICKX ET AL.09308032
	<b>Examiner</b> Brian C Genco	<b>Art Unit</b> 2615

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM  
 THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

1) Responsive to communication(s) filed on 15 February 2005.  
 2a) This action is FINAL.                    2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

4) Claim(s) 3 and 4 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 3 and 4 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1) <input type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____

Applicant's arguments with respect to claims 3 and 4 have been considered but are moot in view of the new ground(s) of rejection.

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 3 and 4 are rejected under 35 U.S.C. 102(b) as being anticipated by (USPN 5,311,320 to Hashimoto).

In regards to claim 3 Hashimoto discloses an image sensor comprising an array of columns and rows of pixels (e.g., column 20, lines 42-43 wherein Hashimoto discloses the pixels are arranged in a matrix form, or array), all of the pixels of one column of the array being connected to at least one common pixel output line (e.g., Fig. 16A, wherein each column would have a common pixel output line in the matrix form as would be recognized by one skilled in the art at the time of the invention) having at least one memory element (e.g., element E of Fig. 16A; column 22, lines 14-38; elements C11 and C12 of Fig. 18) and at least one column output amplifier (e.g., elements A1 – An of Fig. 16A), each common pixel output line being divided through switches (e.g., elements 301A and 303A of Fig. 18) into at least two parallel circuits having said memory element (e.g., Fig. 18), the two parallel circuit being connected through a switch (e.g., elements 302A and 304A of Fig. 18) with the same input of said column amplifying element (e.g., the output of transistors 302A and 304A goes to the column amplifier element A1

as shown in Fig. 18), said column amplifying elements and the common output amplifier (e.g., element 14A of Fig. 16A) being connected by a single bus (e.g., element 101A of Fig. 16A, wherein there is further a switch between said column amplifying element and said bus (e.g., elements T1 – TN of Fig. 16A), and wherein the image sensor is a CMOS or MOS device (e.g., Figs. 16A and 18).

In regards to claim 4 Hashimoto discloses an image sensor as recited in claim 3, wherein both circuits have memory elements (e.g., elements C11 and C12 of Fig. 18).

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian C. Genco who can be reached by phone at 571-272-7364 or by fax at 571-273-7364. The examiner can normally be reached on Monday thru Friday 8:30am to 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, James Groody can be reached at 571-272-7950. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the customer service office whose telephone number is 571-272-2600.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

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system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Brian C Genco  
Examiner  
Art Unit 2615

April 13, 2005



TUAN HO  
PRIMARY EXAMINER